

PTO-1449 REPRODUCED INFORMATION DISCLOSURE CITATION IN AN APPLICATION October 27, 2003 (Use several sheets if necessary)	ATTORNEY DOCKET NO. 2789.2003-001		DIV. OF APPLICATION NO. 09/557,564	
	APPLICANT William J. Dally, <i>et al.</i>			
	FILING DATE	CONFIRMATION NO.	GROUP 2609	

U.S. PATENT DOCUMENTS				
EXAM- INER INI- TIAL	REF. NO.	DOCUMENT NUMBER	ISSUE DATE / PUBLICATION DATE	NAME
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/EO/	AB	5,361,254	11-1994	Storck et al.
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
/EC/	AR	Dally, William J. and Poulton, John W., <i>Digital Systems Engineering</i> , Cambridge University Press, 1998, pp. 428-447, 537-540 and 547-548.
/EC/	AS	Kim, Weigant and Gray, "PLL/DLL System Noise Analysis for Low Jitter Clock Synthesizer Design," ISCAS, 1994, pp. 31-34.
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/EC/	AU	Dally, William J. and Poulton, John W., "Transmitter Equalization for 4Gb/s Signaling," IEEE Micro, Jan-Feb 1997, pp. 48-56.
/EC/	AV	"Engineering and Operations in the Bell System," Second Edition, R.F. Rey, Technical Editor, AT&T Bell Laboratories, Murray Hill, N.J., 1982-1983, pp. 386-393
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